

LT 150396318US #2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

11002 U.S. PTO  
09/04/398  
84/27/01

In re application of: **Paul et al.**

§ Group Art Unit: **Unknown**

Serial No.: \_\_\_\_\_

§

§ Examiner: **Unknown**

Filed: \_\_\_\_\_

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§ Attorney Docket No.: **AUS92000858US1**

**For: Method and System for Fault-Tolerant  
Remote Boot in the Presence of Boot Server  
Overload/Failure With Self-Throttling Boot  
Servers**

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**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97**

Hon. Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

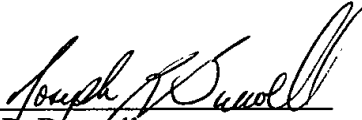
In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined

in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner.

In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. 1.56(a) exists.

Date: March 22, 2001

Respectfully submitted,

  
\_\_\_\_\_  
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| <b>Form PTO-1449</b><br><br><b>LIST OF PRIOR ART CITED BY APPLICANT</b><br><i>(Use several sheets if necessary)</i> |  |  | ATTORNEY DOCKET NO.<br><b>AUS920000858US1</b> |  | SERIAL NO.<br><b>09/</b> |  |
|   |  |  | APPLICANT<br><b>Paul et al.</b>               |  |                          |  |
|   |  |  | FILING DATE                                   |  | GROUP ART UNIT           |  |

**U.S. PATENT DOCUMENTS**

| EXAMINER INITIAL | DOCUMENT NO. | PUBLICATION DATE | INVENTOR NAME | CLASS/SUBCLASS | FILING DATE |
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**FOREIGN PATENT DOCUMENTS**

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**OTHER PRIOR ART (including author, title, date, pertinent page, etc.)**

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|  |  | <b>"Preboot Execution Environment (PXE) Specification Version 2.1", Intel Corp., pp. 1-38, 09/20/1999.</b> |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.